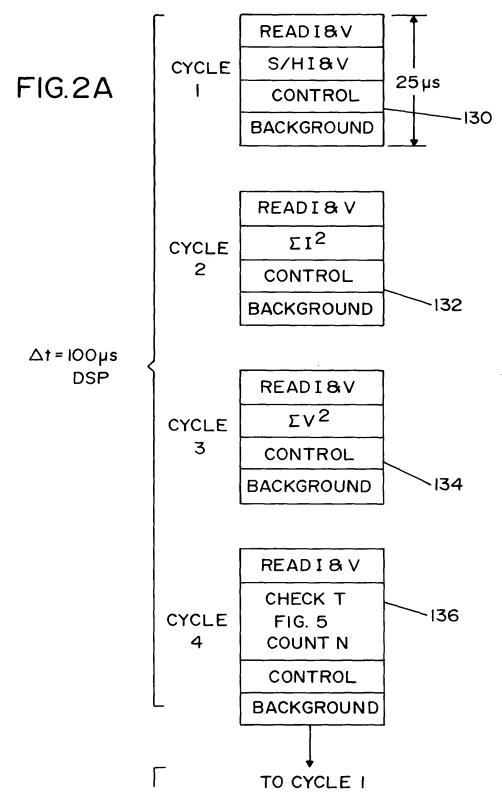


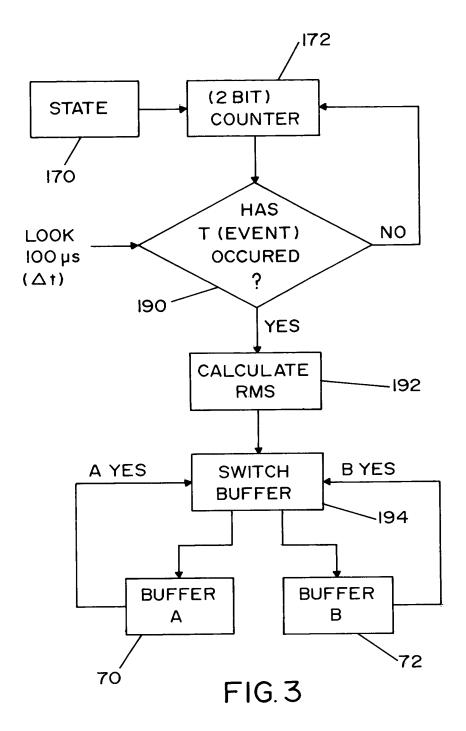
FIG.3A

 $\Gamma_{\mathbf{Q}}^{\mathbf{I}}$

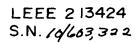




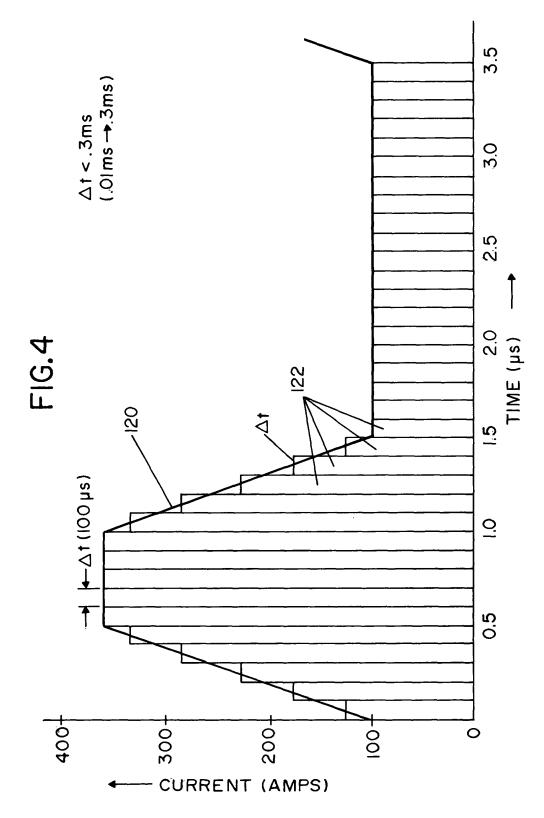
LEEE 2 13424 4/13 S.N. 10/603, 322 G.A.U. 1725

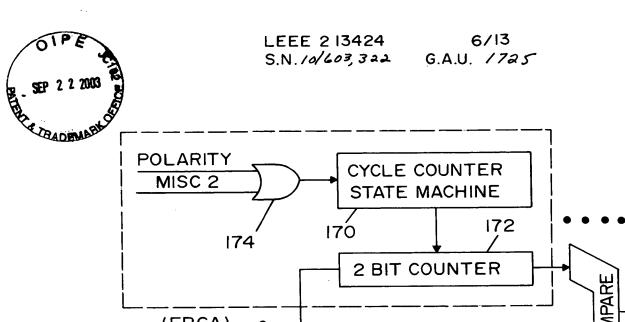






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EVENT T (FPGA) **PREVIOUS** 176 COUNT Δ t 174 LOAD ON **EVENT T** (DSP) 178 FIG.5

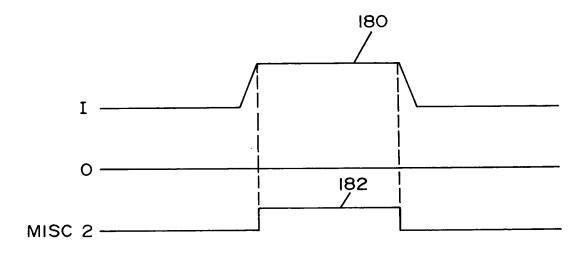
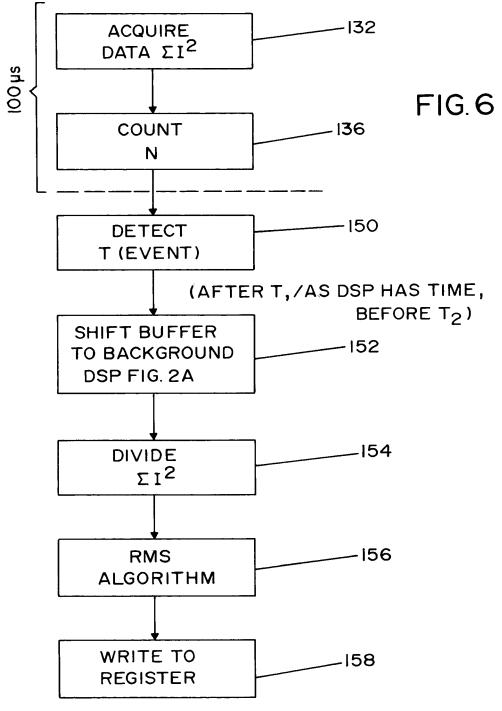
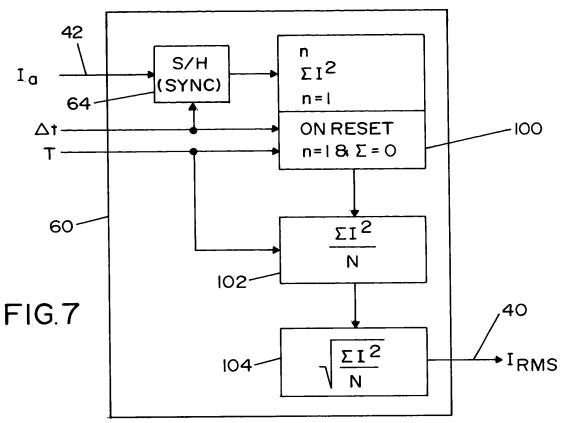
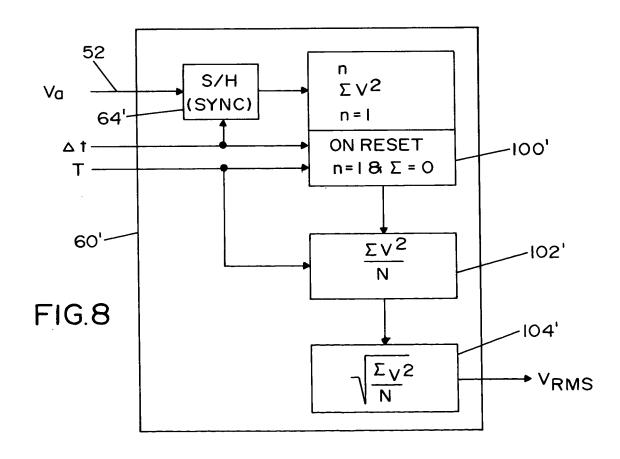


FIG.5A











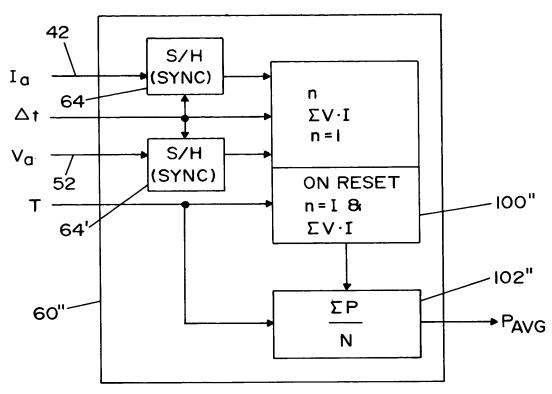
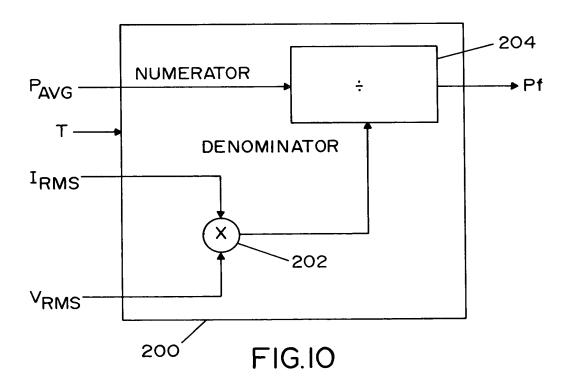


FIG.9



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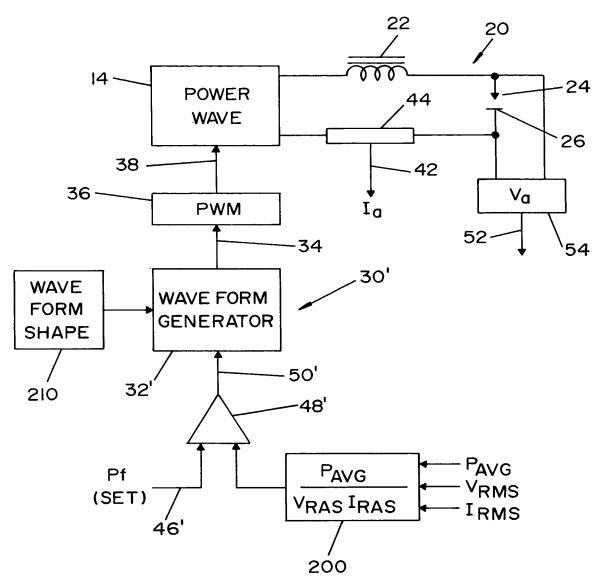
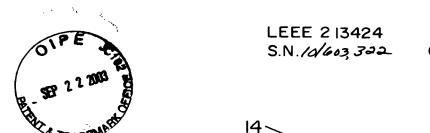


FIG.II



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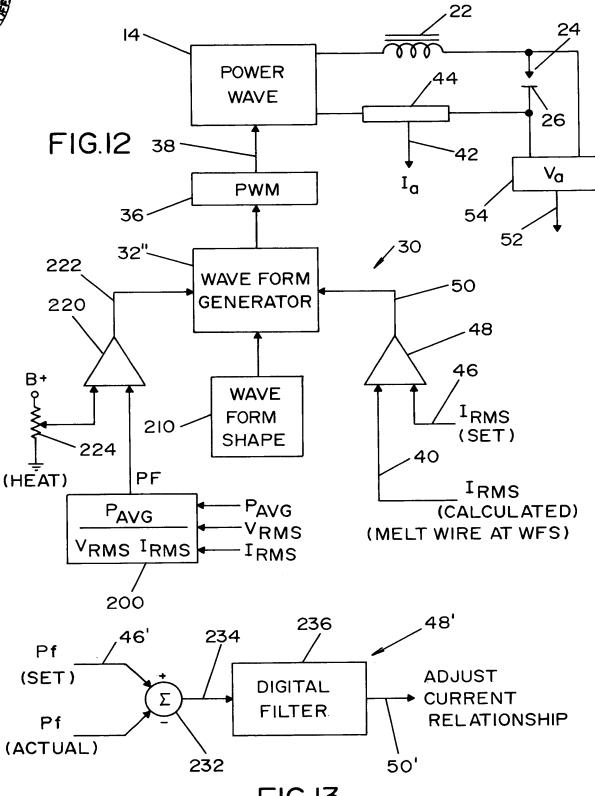


FIG.13



